



Bharatiya Vidya Bhavan's
SARDAR PATEL COLLEGE OF ENGINEERING

(Government Aid ACED Autonomous
Institute) Andheri (W) Mumbai - 400058
ELECTRICAL ENGINEERING DEPARTMENT



TIME TABLE

Academic Year: 2025 - 2026

SEMESTER: I I

M.TECH (PEPS)

Room No. 252

TIME		MONDAY	TUESDAY	WEDNESDAY	THURSDAY	FRIDAY
1	09.00-10.00	Seminar/Mini project	ACED (SA)	PSDC (VD)	RES&GI (TUT) (VD)	ERP (SS)
2	10.00-11.00		PSDC (VD)	SGT (NK)	RES&GI (VD)	
3	11:00-11:30		RECESS			
4	11.30-12.30	RES&GI (VD)	PSDC Lab (VD)	RES&GI (VD)	ACED (SA)	AI (ARK)
5	12.30-01.30	SGT (TUT) (NK)		ACED (SA)	AI (ARK)	
6	01.30-02.:30	RECESS				
7	02.:30-03.30	SGT (NK)	ACED Lab (SA)	PSDC Lab (VD)	PSDC Lab (VD)	ACED Lab (PJ)
8	03.30-04.30					
9	04.30-05.30	I.I.I.	GL	SFS/Rem	SFS/Rem	SGT (TUT)

Course and Course Code				Faculty	
PSDC	Power System Dynamics and Control	PC-MTPX 201 & SE-MTPX201	VD	Prof. Vishal Dhake	
ACACED	Advance Control of Electric Drives	PC-MTPX202 & SE-MTPX202	SA/PJ	Dr. S.H. Abidi / Prof. Prajakta Joshi	
SGT	Smart Grid Technologies	PE-MTPX202	NK	Dr. N. W. Kinhekar	
RES>	Renewable Energy Sources and Grid Integration	PE-MTPX206	VD	Prof. Vishal Dhake	
ERP	English for research paper writing	PE-MTPX107	SS	Dr. Shymalee Solanki	
AI	Artificial Intelligence	OE-MTPX208	ARK	Dr. A.R. Kambekar	
	Seminar/mini Project	PC-MTPX 203			

I.I.I.: Industry Institute Interaction, SFS: Self-study, OC: Online course, Rem: Remedial class, GL: Guest Lecture.

PG Coordinator

Head of the Department

Dean Research & PG

Dean Academics

Principal